

## Refine Search

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Database:

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### Search History

**DATE:** Thursday, January 27, 2005    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
side by side			
	<i>DB=TDBD; PLUR=YES; OP=OR</i>		
<u>L28</u>	<u>L27</u>	0	<u>L28</u>
	<i>DB=DWPI; PLUR=YES; OP=OR</i>		
<u>L27</u>	<u>L26</u>	0	<u>L27</u>
	<i>DB=JPAB; PLUR=YES; OP=OR</i>		
<u>L26</u>	<u>L25</u>	0	<u>L26</u>
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<u>L25</u>	<u>L24</u>	0	<u>L25</u>
	<i>DB=USOC; PLUR=YES; OP=OR</i>		
<u>L24</u>	<u>L23</u>	0	<u>L24</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L23</u>	<u>L22</u>	0	<u>L23</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L22</u>	<u>L21</u>	0	<u>L22</u>

*DB=USPT; PLUR=YES; OP=OR*

L21 "double gate CMOS" and "trench gate" 0 L21

L20 "double gate CMOS" 11 L20

L19 6197672 2 L19

L18 5899710 4 L18

L17 L16 0 L17

L16 "trench gate" same (("first gate" near5 (polysilicon)) same ("second gate" near5 (polysilicon))) 0 L16

*DB=TDBD; PLUR=YES; OP=OR*

L15 L14 0 L15

*DB=JPAB; PLUR=YES; OP=OR*

L14 L13 0 L14

*DB=EPAB; PLUR=YES; OP=OR*

L13 L12 0 L13

*DB=USOC; PLUR=YES; OP=OR*

L12 L11 0 L12

*DB=USPT; PLUR=YES; OP=OR*

L11 "trench gate" same (("first gate" near5 (N near3 polysilicon)) same ("second gate" near5 (P near3 polysilicon))) 0 L11

L10 "trench gate" same ("first gate" near5 "n type") same ("second gate" near5 "p type") 0 L10

*DB=TDBD; PLUR=YES; OP=OR*

L9 L8 0 L9

*DB=DWPI; PLUR=YES; OP=OR*

L8 L7 0 L8

*DB=JPAB; PLUR=YES; OP=OR*

L7 L6 0 L7

*DB=EPAB; PLUR=YES; OP=OR*

L6 L5 0 L6

*DB=PGPB; PLUR=YES; OP=OR*

L5 L4 0 L5

*DB=USOC; PLUR=YES; OP=OR*

L4 L3 0 L4

*DB=USPT; PLUR=YES; OP=OR*

L3 L2 and ("first gate" near6 fermi) and ("second gate" near5 fermi) 0 L3

L2 "trench gate" same (multigate or double gate) 1115 L2

L1 6586290 3 L1

END OF SEARCH HISTORY